

MSD601-RT1, MSD601-ST1

Preferred Device

NPN General Purpose Amplifier Transistors Surface Mount

Features

- Pb-Free Packages are Available

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Value	Unit
Collector – Base Voltage	V _{(BR)CBO}	60	Vdc
Collector – Emitter Voltage	V _{(BR)CEO}	50	Vdc
Emitter – Base Voltage	V _{(BR)EBO}	7.0	Vdc
Collector Current – Continuous	I _C	100	mAdc
Collector Current – Peak	I _{C(P)}	200	mAdc

THERMAL CHARACTERISTICS

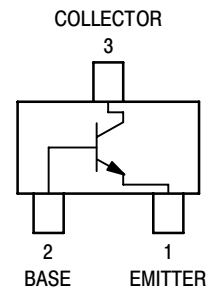
Characteristic	Symbol	Max	Unit
Power Dissipation	P _D	200	mW
Junction Temperature	T _J	150	°C
Storage Temperature	T _{stg}	-55 ~ +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



ON Semiconductor®

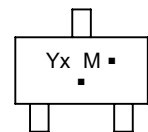
<http://onsemi.com>



MARKING DIAGRAM



SC-59
CASE 318D



- x = R for RT1
S for ST1
 - M = Date Code
 - = Pb-Free Package
- (Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

MSD601–RT1, MSD601–ST1

ELECTRICAL CHARACTERISTICS (T_A = 25°C)

Characteristic	Symbol	Min	Max	Unit
Collector – Emitter Breakdown Voltage (I _C = 2.0 mA _{dc} , I _B = 0)	V _{(BR)CEO}	50	–	V _{dc}
Collector – Base Breakdown Voltage (I _C = 10 μA _{dc} , I _E = 0)	V _{(BR)CBO}	60	–	V _{dc}
Emitter – Base Breakdown Voltage (I _E = 10 μA _{dc} , I _C = 0)	V _{(BR)EBO}	70	–	V _{dc}
Collector – Base Cutoff Current (V _{CB} = 45 V _{dc} , I _E = 0)	I _{CBO}	–	0.1	μA _{dc}
Collector – Emitter Cutoff Current (V _{CE} = 10 V _{dc} , I _B = 0)	I _{CEO}	–	100	nA _{dc}
DC Current Gain (Note 1) (V _{CE} = 10 V _{dc} , I _C = 2.0 mA _{dc}) MSD601–RT1 MSD601–ST1 (V _{CE} = 2.0 V _{dc} , I _C = 100 mA _{dc})	h _{FE1} h _{FE2}	210 290 90	340 460 –	–
Collector – Emitter Saturation Voltage (I _C = 100 mA _{dc} , I _B = 10 mA _{dc})	V _{CE(sat)}	–	0.5	V _{dc}

1. Pulse Test: Pulse Width ≤ 300 μs, D.C. ≤ 2%.

ORDERING INFORMATION

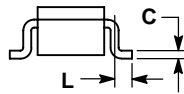
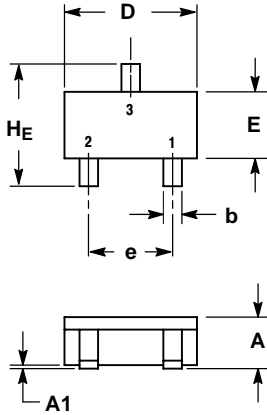
Device	Package	Shipping†
MSD–601RT1	SC–59	3000 Units / Reel
MSD–601RT1G	SC–59 (Pb–Free)	3000 Units / Reel
MSD–601ST1	SC–59	3000 Units / Reel
MSD–601ST1G	SC–59 (Pb–Free)	3000 Units / Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MSD601-RT1, MSD601-ST1

PACKAGE DIMENSIONS

SC-59
CASE 318D-04
ISSUE G



NOTES:

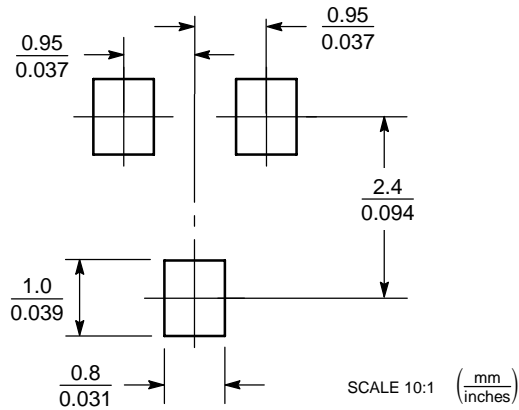
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.00	1.15	1.30	0.039	0.045	0.051
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.35	0.43	0.50	0.014	0.017	0.020
c	0.09	0.14	0.18	0.003	0.005	0.007
D	2.70	2.90	3.10	0.106	0.114	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
e	1.70	1.90	2.10	0.067	0.075	0.083
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.80	3.00	0.099	0.110	0.118

STYLE 1:

1. EMITTER
2. BASE
3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MSD601-RT1, MSD601-ST1

ON Semiconductor and **ON** are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
P.O. Box 61312, Phoenix, Arizona 85082-1312 USA
Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada
Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051
Phone: 81-3-5773-3850

ON Semiconductor Website: <http://onsemi.com>

Order Literature: <http://www.onsemi.com/litorder>

For additional information, please contact your local Sales Representative.